

**ST. XAVIER'S TECHNICAL INSTITUTE, MAHIM**

**WINTER 2018 TERM END THEORY EXAMINATION SCHEDULE**

**ODD SEMESTER (REGULAR + BACKLOG)**

**Morning Session 9.30 a.m - 12.30 p.m.**

Semester I	Day	Monday	Tuesday	Wednesday	Thursday	Friday
	Date	10/12/2018	11/12/2018	12/12/2018	13/12/2018	14/12/2018
	Course Name		Basic Mathematics		Basic Electronics / Basic Electronic Devices	
	Course Code		ET-18111 / ET-15111 / ET-11111 / ET-101 / EV- 101		ET-18121 / ET-15112 / ET-11112	
Semester III	Course Name	Applied Mathematics		Principles of Communication I		Electronics Test Instruments / Electronic Measurements and Instruments
	Course Code	ET - 15311 /ET-11311 / ET-301 / EV-105		ET-15312 / ET-11312 / ET-302		ET-15313 /ET-11313 / ET- 303/ EV-208
Semester V	Course Name		Microprocessor and Microcontrollers/Mi croprocessors and Peripherals / Microprocessors		Signals and Systems	
	Course Code		ET - 15519 / ET-11511/ EV-310		ET-15512 / ET-11512/ EV-516	
Semester I	Day	Saturday	Monday	Tuesday	Wednesday	Thursday
	Date	15/12/2018	17/12/2018	18/12/2018	19/12/2018	20/12/2018
	Course Name	Basic Electrical Engineering		English Language		
	Course Code	ET-18113 / ET-15113 / ET-11113		ET-18118 / ET-15118 / ET-11118		
Semester III	Course Name		Linear Integrated Circuits		C' Programming / Advanced C Programming (Backlog)	
	Course Code		ET-15315 / ET-11315		ET-11314 / ET - 305	
Semester V	Course Name	Advanced Communication Systems		Basic Control Systems / Industrial Electronics		
	Course Code	ET-15513 / ET-11513		ET-15520 / ET-11515		



Dr. S. B. Ghungrad  
Principal



Mestry Dipti  
Controller of Examination

Cc: Office I/C.H.O.D Website Notice Board

**Note: Any correction still needed in the time table can be communicated along with justification to Examination Section till 16th November 2018.**

**Examination of different semesters scheduled on the same day but in different slots shall not be considered as clash.**