

**ST. XAVIER'S TECHNICAL INSTITUTE, MAHIM**


**WINTER 2017 TERM END THEORY EXAMINATION SCHEDULE**

**ODD SEMESTER (REGULAR + BACKLOG)**

**Morning Session 9.30 a.m - 12.30 p.m.**

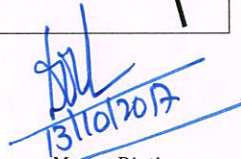
Day	Wednesday	Thursday	Friday	Monday	Tuesday
<b>Date</b>	<b>22/11/2017</b>	<b>23/11/2017</b>	<b>24/11/2017</b>	<b>27/11/2017</b>	<b>28/11/2017</b>
<b>Semester I</b>		Basic Mathematics		Basic Electronic Devices	
Course Code		ET-15111 / ET-11111 / ET-101 / EV-101		ET-15112 / ET-11112	
<b>Semester III</b>	Applied Mathematics		Principles of Communication I		Electronics Test Instruments / Electronic Measurements and Instruments
Course Code	ET-11311 / ET - 15311 / ET-301 / EV-105		ET-11312 / ET-15312 / ET-302		ET-11313 / ET-15313 / ET- 303
<b>Semester V</b>		Microprocessor and Microcontrollers/Microprocessors and Peripherals		Signals and Systems	
Course Code		ET - 15519 / ET-11511		ET-15512 / ET-11512	

Day	Wednesday	Thursday	Monday	Tuesday	
<b>Date</b>	<b>29/11/2017</b>	<b>30/11/2017</b>	<b>4/12/2017</b>	<b>5/12/2017</b>	
<b>Semester I</b>	Basic Electrical Engineering		English Language		
Course Code	ET-15113 / ET-11113		ET-15118 / ET-11118		
<b>Semester III</b>		Linear Integrated Circuits		C' Programming / Advanced C Programming (Backlog)	
Course Code		ET-11315 / ET-15315		ET-11314 / ET - 305	
<b>Semester V</b>	Advanced Communication Systems		Basic Control Systems (Regular + Backlog)		
Course Code	ET-15513 / ET-11513		ET-15520 / ET-11414		

  
Dr. S. B. Ghungrad  
Principal

Office

  
H.O.D

  
Mestry Dipti  
Controller of Examination

Notice Board

**Note: Any correction still needed in the time table can be communicated along with justification to Examination Section till 27th October 2017.**

**Examination of different semesters scheduled on the same day but in different slots shall not be considered as clash.**