

**ST. XAVIER'S TECHNICAL INSTITUTE, MAHIM**  
**WINTER 2020 TERM END THEORY EXAMINATION SCHEDULE**

ODD AND EVEN SEMESTER (REGULAR + BACKLOG)

**Morning Session 10.30 a.m - 11.30 a.m.**

Day	Tuesday	Wednesday	Thursday	Friday	Monday	Tuesday	Wednesday	Thursday	Friday
Date	23/02/2021	24/02/2021	25/02/2021	26/02/2021	01/03/2021	02/03/2021	03/03/2021	04/03/2021	05/03/2021
<b>Semester I</b>		Basic Mathematics		Basic Electronics		Basic Electrical Engineering		English Language	
Course Code		ET-18111 / ET-15111 / ET-11111 / ET-101 / EV-101		ET-18112		ET-18113 / ET-15113 / ET-11113		ET-18118 / ET-15118 / ET-11118	
<b>Semester II</b>	Engineering Mathematics / Advanced Engineering Mathematics		Applied Electronics		Electronic Circuits & Applications		Electrical Machines		
Course Code	ET-18211 / ET-15211 /ET-11211 / ET-201/ EV-103		ET-18222		ET-18223		ET-18216 / ET-11216 / ET-15216		
<b>Semester V</b>		Microprocessor and Microcontrollers/ Microprocessors and Peripherals/ Microprocessor		Signals and Systems		Advanced Communication Systems		Basic Control Systems (Regular + Backlog)	
Course Code		ET - 18519 / ET - 15519 / ET-11511 / EV-310		ET-18512 / ET-15512 / ET-11512 / EV - 516		ET-18513 / ET-15513 / ET-11513		ET-18520 / ET-15520 / ET- 11414	
<b>Semester VI</b>	Industrial Management and Quality Control		Digital Signal Processing		Digital Communication		Data Communication & Computer Networking / Colour TV		Microcontroller and Applications / Advanced Power Electronics
Course Code	ET-15620		ET-15612 / ET-11612		ET-15614 / ET-11614 / EV- 501		ET-15613 / ET-11613/ EV- 316		EV-506 / ET -11617 / ET-15617/

**Afternoon Session 2.30 p.m. - 3.30 p.m.**

Day	Tuesday	Wednesday	Thursday	Friday	Monday	Tuesday	Wednesday	Thursday	Friday
Date	23/02/2021	24/02/2021	25/02/2021	26/02/2021	01/03/2021	02/03/2021	03/03/2021	04/03/2021	05/03/2021
<b>Semester III</b>	Applied Mathematics		Principles of Communication I		Electronics Test Instruments / Electronic Measurements and Instruments		Linear Integrated Circuits		C' Programming / Advanced C Programming (Backlog)
Course Code	ET-18311 / ET-15311 / ET-11311 / ET-301 / EV-105		ET-18312 / ET-15312 / ET-11312 / ET-302		ET-18313 / ET-15313 / ET-11313 / EV-208 ET-303		ET-18315 / ET-15315 / ET-11315		ET-11314 / ET - 305 / EV - 512
<b>Semester IV</b>		Entrepreneurship / Industrial Electronics		Principles of Communication - II		Digital Electronics / Electronic Circuit Design		Circuits and Networks	
Course Code		ET-11411 / EV-401 ET-11515/ ET-15420 / ET -18420		ET-18312 / ET-15412 / ET-11412		ET-18413 / ET-15413 / ET- 11413 EV-308		ET-18415 / ET-15415 / ET- 11415 / ET-304	

Sd/-  
Dr. S. B. Ghungrad  
Principal

Sd/-  
Mestry Dipti  
Controller of Examination

Cc: Office

H.O.D

Notice Board

**Note: If any course is getting overlapped in the time table write to the Examination Section at examination@xaviertech.com along with justification till 10th February 2021.**

**Examination of different semesters scheduled on the same day but in different time slots shall not be considered as clash.**