

TENTATIVE WINTER 2022 TERM END BACKLOG PRACTICAL EXAMINATION SCHEDULE

Day	Date	Session	First year	Second Year
MONDAY	02-01-23	Morning 9.00 am to 2.00 pm	Basic Electronics ET-18121	Electronic Test Instruments ET-18313
			Electrical Machines ET-18216	Principles of Communication -II ET-18412
TUESDAY	03-01-23	Morning 9.00 am to 2.00 pm	Applied Electronic ET-18222	Circuits and Networks ET- 18415
			Basic Electrical Engineering ET-18113	Entrepreneurship ET-18411 MCQ TYPE EXAMINATION (Refer to detailed schedule)
WEDNESDAY	04-01-23	Morning 9.00 am to 2.00 pm	Electronic Materials and Components ET- 18116 MCQ TYPE EXAMINATION (Refer to detailed schedule)	Digital Electronics ET-18413
THURSDAY	05-01-23	Refer to detailed schedule	Computer Application ET- 18115 MCQ TYPE EXAMINATION	-----
			Environmental Science ET-18219 MCQ TYPE EXAMINATION	-----
FRIDAY	06-01-23	Morning 10.00 am to 1.00 pm	-----	C Programming ET-18314 MCQ TYPE EXAMINATION (Refer to detailed schedule)
		Morning 9.00 am to 2.00 pm	-----	Industrial Electronics ET-18420
MONDAY	09-01-23	Morning 10.00 am to 1.00 pm.	Engineering Drawing and C.A.D ET-18215	-----
		Morning 9.00 am to 2.00 pm	Electronic Circuits and Applications ET - 18223	-----

Sd/-
Dr. S. B. Ghungrad
Principal

Sd/-
Mestry dipti
Controller of Examination

CC: Office

H.O.D

Website

NOTE:

Any correction still needed in the time table can be communicated along with justification to Examination Section till 27th December 2022.

Online Practical examination will be conducted for 1 and 1/2 (one and half) hour only during the time slots mentioned in the time table.

All the candidates have to report half hour before their practical examination timings mentioned in the time table.

Backlog candidates will appear for their respective courses mentioned in this time table along with the regular candidates after consulting with the internal examiner regarding the time slot.

Examination of different semesters scheduled on the same day but in different slots shall not be considered as clash.

All the Online Examinations will be conducted in Computer Laboratory and PCB Designing Laboratory