

ST. XAVIER'S TECHNICAL INSTITUTE, MAHIM
FINAL SUMMER 2023 TERM END THEORY EXAMINATION SCHEDULE

ODD AND EVEN SEMESTER (REGULAR + BACKLOG)

Morning Session 9.30 AM To 12.30 AM

Day	Semester	Tuesday 23-May-23	Wednesday 24-May-23	Thursday 25-May-23	Friday 26-May-23	Monday 29-May-23	Tuesday 30-May-23	Wednesday 31-May-23	Thursday 01-Jun-23	Friday 02-Jun-23
Course Name	Semester II			Engineering Mathematics		Applied Electronics		Electronic Circuits & Applications		Electrical Machines
Course Code				ET-18211 / ET-15211 / ET-11211		ET-18222		ET-18223		ET-18216 / ET-15216 / ET-11216
Course Name		Mathematics - II				Electrical Devices & Machines				Electronics Devices & Circuits
Course Code		ETBS201				ETES203				ETPC204
Course Name	Semester IV / V		Principles of Communication -II		Digital Electronics		Circuits and Networks		Industrial Electronics	
Course Code			ET-18412 / ET-15412 / ET-11412		ET-18413 / ET-15413 / ET-11413		ET-18415 / ET-15415 / ET-11415		ET-18420 / ET-15420 / ET-11515 /	
Course Name	Semester VI	Industrial Management and Quality Control		Digital Signal Processing		Data Communication & Computer Networking		Digital Communication		Advanced Power Electronics
Course Code		ET-18620 / ET-15620		ET-18612 / ET-15612 / ET-11612		ET-18613 / ET-15613 / ET-11613		ET-18614 / ET-15614 / ET-11614		ET-18617 / ET-15617

Afternoon Session 2.00 PM To 5.00 PM

Course Name	Semester I		Basic Mathematics		Basic Electronics		Basic Electrical Engineering		English Language	
Course Code			ET-18111 / ET-15111 / ET-11111		ET-18121		ET-18113 / ET-15113 / ET-11113		ET-18118 / ET-15118 / ET-11118	
Course Name			Mathematics I		Basic Electronics		Fundamental of Electrical Networks			
Course Code			ETBS101		ETPC102		ETES103			
Course Name	Semester III	Applied Mathematics		Principles of Communication I				Linear Integrated Circuits		Electronics Test Instruments
Course Code		ET-18311 / ET-15311 / ET-11311		ET-18312 / ET-15312 / ET-11312				ET-18315 / ET-15315 / ET-11315		ET-18313 / ET-15313 / ET-11313
Course Name	Semester V		Microprocessor and Microcontrollers		Signals and Systems		Advanced Communication Systems		Basic Control Systems (Regular + Backlog)	
Course Code			ET - 18519		ET-18512 / ET-11512		ET-18513 / ET-15513 / ET-11513		ET-18520 / ET-15520 / ET-11414	

Sd/-
Dr. S. B. Chungrad
Principal

Sd/-
Mestry Dipti
Controller of Examination

Cc: Office

H.O.D

Examination Notice Board

Note: Examination of different semesters scheduled on the same day but in different time slots shall not be considered as clash.

For any query in the time table write to the Examination Section at examination@xavitech.com