

## ST. XAVIER'S TECHNICAL INSTITUTE, MAHIM

### TENTATIVE SUMMER 2023 TERM END BACKLOG PRACTICAL EXAMINATION SCHEDULE

Day	Date	Session	Semester	Course Name and Course Code
MONDAY	15-05-23	3.00 PM TO 4.30 PM	One	Fundamentals of Electrical Networks ETES103
			One	Basic Electronics ET-18121
			Two	Applied Electronic ET-18222
			Five	Advance Communication Systems ET-18513
TUESDAY	16-05-23	3.00 PM TO 4.30 PM	Two	Electronic Circuits and Applications ET -18223
			Two	Electrical Machines ET-18216
			Two	Environmental Science ET-18219 MCQ TYPE EXAMINATION
			One	Electronic Materials and Components ET-18116 MCQ TYPE EXAMINATION
			Three	Principles of Communication I ET-18312
			Three	Electronic Test Instruments ET-18313
			Five	Signals and Systems ET-18512

Day	Date	Session	Semester	Course Name and Course Code
WEDNESDAY	17-05-23	3.00 PM TO 4.30 PM	Three	Linear Integrated Circuits ET-18315
			Five	Microprocessor & Microcontrollers ET - 18519
		11.00 AM TO 1.00 PM	Five	Basic Control Systems ET-18520
THURSDAY	18-05-23	3.00 PM TO 4.30 PM	One	Computer Application ET- 18115 MCQ TYPE EXAMINATION
			Two	Engineering Drawing and C.A.D ET- 18215
			Five	Circuit Simulation and PCB Designing ET- 18517
FRIDAY	19-05-23	3.00 PM TO 4.30 PM	Three	C Programming ET-18314
			Five	Vocational Training ET- 18516
MONDAY	22-05-23	9.00 AM TO 10.30AM AND 10.45 AM TO 12.00 NOON	One	Introduction to IT ETES104
		12.00 NOON TO 2.00 PM	One	Basic Electronics ETPC102

Sd/-  
Dr. S. B. Ghungrad  
Principal

Sd/-  
Mestry dipti  
Controller of Examination

CC: Office H.O.D Examination Notice board

**NOTE:**

For any issue in relation to examination write to Examination Section at [examination@xaviertech.com](mailto:examination@xaviertech.com) till 10th May 2023.

MCQ Type Practical examination will be conducted for 1 and 1/2 ( one and half ) hour only during the time slots mentioned in the time table.

All the candidates have to report half hour before their practical examination timings mentioned in the time table.

Backlog candidates will appear for their respective courses mentioned in this time table along with the regular candidates after consulting with the internal examiner regarding the time slot.

Examination of different semesters scheduled on the same day but in different slots shall not be considered as clash.

MCQ type Practical examination will be conducted in Microprocessor and Microcontroller laboratory (3rd Floor) and Printed Circuit Board laboratory ( 2nd floor)

If at any point of time, before, during or after the examination, it is found / reported that a candidate(s) has indulged in any act of misbehavior or misconduct, thereby committing breach of any of the rules laid down by the Board / institute, all such cases will be placed before the Special Committee for its final decision on penalties to be imposed or action to be taken in this regard.