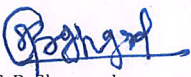
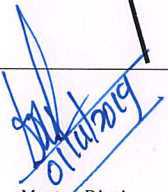


ST. XAVIER'S TECHNICAL INSTITUTE, MAHIM					
WINTER 2019 TERM END THEORY EXAMINATION SCHEDULE					
ODD SEMESTER (REGULAR + BACKLOG)					
Morning Session 9.30 a.m - 12.30 p.m.					
Day	Tuesday	Wednesday	Thursday	Friday	Monday
Date	3/12/2019	4/12/2019	5/12/2019	6/12/2019	9/12/2019
Semester I		Basic Mathematics		Basic Electronics / Basic Electronic Devices	
Course Code		ET-18111 / ET-15111 / ET-11111 / ET-101 / EV- 101		ET-18112 / ET-15112 / ET-11112	
Semester III	Applied Mathematics		Principles of Communication I		Electronics Test Instruments / Electronic Measurements and Instruments
Course Code	ET - 15311/ ET-11311 / ET-301 / EV-105		ET-15312 / ET-11312 / ET-302		ET-15313 / ET-11313 / ET- 303 / EV - 208
Semester V		Microprocessor and Microcontrollers/ Microprocessors and Peripherals/ Microprocessor		Signals and Systems	
Course Code		ET - 15519 / ET-11511 / EV-310		ET-15512 / ET-11512 / EV - 516	
Day	Tuesday	Wednesday	Thursday	Thursday	
Date	10/12/2019	11/12/2019	12/12/2019	13/12/2019	
Semester I	Basic Electrical Engineering		English Language		
Course Code	ET-18113 / ET-15113 / ET-11113		ET-18118 / ET-15118 / ET-11118		
Semester III		Linear Integrated Circuits		C' Programming / Advanced C Programming (Backlog)	
Course Code		ET-11315 / ET-15315		ET-11314 / ET - 305	
Semester V	Advanced Communication Systems		Basic Control Systems (Regular + Backlog)		
Course Code	ET-15513 / ET-11513		ET-15520 / ET-11414		


Dr. S. B. Ghungrad
Principal


Mestry Dipti
Controller of Examination

Cc: Office H.O.D Website

Note: Any correction still needed in the time table can be communicated along with justification to Examination Section till 13th November 2019.

Examination of different semesters scheduled on the same day but in different slots shall not be considered as clash.