

ST. XAVIER'S TECHNICAL INSTITUTE, MAHIM
TENTATIVE WINTER 2022 TERM END THEORY EXAMINATION SCHEDULE

ODD AND EVEN SEMESTER (REGULAR + BACKLOG)

Morning Session 09.30 a.m - 12.30 p.m.

Day	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY
Date	10-01-23	11-01-23	12-01-23	13-01-23	16-01-23	17-01-23	18-01-23	19-01-23	20-01-23
Semester I		Basic Mathematics		Basic Electronics		Basic Electrical Engineering		English Language	
Course Code		ET-18111 / ET-15111 / ET-11111		ET-18121		ET-18113 / ET-15113 / ET-11113		ET-18118 / ET-15118 / ET-11118	
Semester I		Mathematics I		Basic Electronics		Fundamental of Electrical Networks		
Course Code		ETBS101		ETPC102		ETES103		
Semester II	Engineering Mathematics		Applied Electronics		Electronic Circuits & Applications		Electrical Machines		
Course Code	ET-18211 / ET-15211 / ET-11211		ET-18222		ET-18223		ET-18216 / ET-15216 / ET-11216		
Semester V		Microprocessor and Microcontrollers		Signals and Systems		Advanced Communication Systems		Basic Control Systems (Regular + Backlog)	
Course Code		ET - 18519 / ET - 15519		ET-18512 / ET-15512 / ET-11512		ET-18513 / ET-15513 / ET-11513		ET-18520 / ET-15520 / ET-11414	
Semester VI	Industrial Management and Quality Control		Digital Signal Processing		Digital Communication		Data Communication & Computer Networking		Advanced Power Electronics
Course Code	ET-18620 / ET-15620		ET-18612 / ET-15612 / ET-11612		ET-18614 / ET-15614 / ET-11614		ET-18613 / ET-15613 / ET-11613		ET-18617 / ET-15617

Afternoon Session 2.00 p.m. - 5.00 p.m.

Day	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY
Date	10-01-23	11-01-23	12-01-23	13-01-23	16-01-23	17-01-23	18-01-23	19-01-23	20-01-23
Semester III	Applied Mathematics		Principles of Communication I		Electronics Test Instruments		Linear Integrated Circuits		
Course Code	ET-18311 / ET-15311 / ET-11311		ET-18312 / ET-15312 / ET-11312		ET-18313 / ET-15313 / ET-11313		ET-18315 / ET-15315 / ET-11315		
Semester IV		Industrial Electronics		Principles of Communication -II		Digital Electronics		Circuits and Networks	
Course Code		ET-18420 / ET-15420 / ET-11515		ET-18412 / ET-15412 / ET-11412 /		ET-18413 / ET-15413 / ET-11413		ET-18415 / ET-15415 / ET-11415	

Sd/-
Dr. S. B. Ghungrad
Principal

Sd/-
Mestry Dipti
Controller of Examination

Cc: Office

H.O.D

Notice Board

Note: If any course is getting overlapped in the time table write to the Examination Section at examination@xaviertech.com along with justification till 27th December 2022.

All the candidates have to report half hour before their practical examination timings mentioned in the time table.

Examination of different semesters scheduled on the same day but in different time slots shall not be considered as clash.