

ST. XAVIER'S TECHNICAL INSTITUTE, MAHIM

FINAL SUMMER 2023 EVEN SEMESTER TERM END PRACTICAL EXAMINATION SCHEDULE (Regular + Backlog)

Day and Date	Timing for First year	FIRST YEAR					SECOND YEAR					THIRD YEAR					
		Electronics Devices & Circuits ETPC204	Electrical Devices & Machines ETES203	Electronic Components and Sensors ETES202	Engineering Graphics ETES206	Introduction to Programming ETPC205	Timing for Second year	Entrepreneurship ET-18411	Principles of Communication II ET-18412	Industrial Electronics ET-18420	Digital Electronics ET-18413	Circuits and Networks ET-18415	Timing for Third year	Digital Communication ET-18614	Digital Signal Processing ET-18612	Data Communication and Computer Networking ET-18613	Project II ET-18616
Monday 15/05/2023	9.00 AM-3.00 PM	DIV A	DIV B				---	Refer to detailed sheet attached for Online practical Examination for the examinees of Second Year regular and backlog if any				9.00 AM-3.00 PM	DIV A	---			
Tuesday 16/05/2023	9.00 AM-3.00 PM	DIV B	DIV A				9.00 AM-3.00 PM		DIV A			9.00 AM-3.00 PM		DIV A			
Wednesday 17/05/2023	---						9.00 AM-3.00 PM		DIV B			9.00 AM-3.00 PM					
Thursday 18/05/2023	9.00 AM-3.00 PM						9.00 AM-3.00 PM					9.00 AM-3.00 PM					
Friday 19/05/2023	9.00 AM-3.00 PM						9.00 AM-3.00 PM					9.00 AM-3.00 PM					
Monday 22/05/2023	9.00 AM-3.00 PM						9.00 AM-3.00 PM					9.00 AM-3.00 PM					DIV A

Sd/-
Dr. S. B. Ghungrad
Principal

Sd/-
Mestry Dipti
Controller of Examination

Cc: H.O.D Office Examination Notice board

Note for the candidates:	For any issue in relation to examination write to Examination Section at examination@xaviertech.com
	MCQ Type Practical examination will be conducted for 1 and 1/2 (one and half) hour only during the time slots mentioned in the time table.
	All the candidates have to report half hour before their practical examination timings mentioned in the time table.
	Backlog candidates will appear for their respective courses mentioned in this time table along with the regular candidates after consulting with the internal examiner regarding the time slot.
	Examination of different semesters scheduled on the same day but in different slots shall not be considered as clash.
	MCQ type Practical examination will be conducted in Microprocessor and Microcontroller laboratory (3rd Floor) and Printed Circuit Board laboratory (2nd floor)
	If at any point of time, before, during or after the examination, it is found / reported that a candidate(s) has indulged in any act of misbehavior or misconduct, thereby committing breach of any of the rules laid down by the Board / institute, all such cases will be placed before the Special Committee for its final decision on penalties to be imposed or action to be taken in this regard.